

## INTERNATIONAL SEARCH REPORT

International Application No

PCT/US2004/029083

## A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 G06F9/45

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, INSPEC, WPI Data

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>KRISHNASWAMY A ET AL: "Profile guided selection of ARM and thumb instructions" SIGPLAN NOTICES ACM USA, vol. 37, no. 7, July 2002 (2002-07), pages 56-64, XP002309544  ISSN: 0362-1340  page 56, right-hand column, paragraph 3 -  page 59, left-hand column, paragraph 5  page 58, right-hand column, paragraph 4 -  page 60, right-hand column, paragraph 1  -----  -/-</p>	1-30



Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

## \* Special categories of cited documents:

\*A\* document defining the general state of the art which is not considered to be of particular relevance

\*E\* earlier document but published on or after the international filing date

\*L\* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

\*O\* document referring to an oral disclosure, use, exhibition or other means

\*P\* document published prior to the international filing date but later than the priority date claimed

\*I\* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

\*X\* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

\*Y\* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

\*S\* document member of the same patent family

Date of the actual completion of the international search

14 January 2005

Date of mailing of the international search report

07/02/2005

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## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>KRISHNASWAMY A ET AL: "MIXED-WIDTH INSTRUCTION SETS"</p> <p>COMMUNICATIONS OF THE ASSOCIATION FOR COMPUTING MACHINERY, ASSOCIATION FOR COMPUTING MACHINERY. NEW YORK, US, vol. 46, no. 8, August 2003 (2003-08), pages 47-52, XP001170369</p> <p>ISSN: 0001-0782</p> <p>page 47</p> <p>page 49, left-hand column, paragraph 4 -</p> <p>page 50, right-hand column, paragraph 2</p>	1-30
A	<p>FISCHER H -J ET AL: "Optimal mixing of 16 bit and 32-bit computer codes by means of "CodeBalance""</p> <p>ELEKTRONIK WEKA-FACHZEITSCHRIFTEN GERMANY, vol. 48, no. 19, 21 September 1999 (1999-09-21), pages 96-99, XP001204044</p> <p>ISSN: 0013-5658</p> <p>page 96, left-hand column, paragraph 1 -</p> <p>page 97, left-hand column, paragraph 2</p> <p>page 97, right-hand column, paragraph 4 -</p> <p>page 98, right-hand column, paragraph 1</p>	1-3, 8-10, 13-19, 21,22, 24-30
A	<p>HALAMBI A ET AL: "An efficient compiler technique for code size reduction using reduced bit-width ISAs"</p> <p>PROCEEDINGS 2002 DESIGN, AUTOMATION AND TEST IN EUROPE CONFERENCE AND EXHIBITION IEEE COMPUT. SOC LOS ALAMITOS, CA, USA, March 2002 (2002-03), pages 402-408, XP002309840</p> <p>ISBN: 0-7695-1471-5</p> <p>abstract</p> <p>page 402, right-hand column, paragraph 2 -</p> <p>page 403, left-hand column, paragraph 1</p> <p>page 403, left-hand column, paragraph 3 -</p> <p>page 404, right-hand column, paragraph 2</p> <p>page 405, left-hand column, paragraph 3 -</p> <p>page 407, left-hand column, paragraph 5</p>	1,8-10, 13-17, 21,22, 24-30
A	<p>DELSART B ET AL: "JCOD: a lightweight modular compilation technology for embedded Java"</p> <p>LECTURE NOTES IN COMPUTER SCIENCE, SPRINGER VERLAG, NEW YORK, NY, US, vol. 2491, October 2002 (2002-10), pages 197-212, XP002300520</p> <p>ISSN: 0302-9743</p> <p>page 198, paragraph 1 - page 199, paragraph 5</p> <p>page 200, paragraph 3</p> <p>page 204, paragraph 5</p> <p>page 205, paragraph 2</p>	1,3,5,6, 8-11,17, 19-23, 28-30

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## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>CLARK D: "Mobile processors begin to grow up" COMPUTER IEEE COMPUT. SOC USA, vol. 35, no. 3, March 2002 (2002-03), pages 22-25, XP002309545 ISSN: 0018-9162 page 22, left-hand column, paragraph 2 - paragraph 3 page 23, left-hand column, paragraph 7 - middle column, paragraph 2 page 23, middle column, paragraph 8 - right-hand column, paragraph 7 page 25, left-hand column, paragraph 2 - paragraph 4</p> <p>-----</p>	<p>1,5-10, 17, 20-22, 28-30</p>

